·	Application No.	Applicant(s)
Notice of Allowability	09/956,994	KIMURA, MUTSUMI
	Examiner	Art Unit
	David L. Lewis	2629
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>5/18/2007</u> .		
2.   The allowed claim(s) is/are 1 and 3-16.		
3.		
Attachment(s)  1. Notice of References Cited (PTO-892)  2. Notice of Draftperson's Patent Drawing Review (PTO-948)  3. Information Disclosure Statements (PTO/SB/08),	5. ☐ Notice of Informal P 6. ☐ Interview Summary Paper No./Mail Dat 7. ☐ Examiner's Amendr	(PTO-413), re
Paper No./Mail Date  4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ⊠ Examiner's Stateme 9. □ Other	ent of Reasons for Allowance

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## **REASONS FOR ALLOWANCE**

- 1. Claims 1 and 3-16 are allowed over the prior art of record.
- 2. Claim 2 has been cancelled.
- The following is an examiner's statement of reasons for allowance: The Applicant 3. arguments filed on 11/22/2006 with respect to claims 1 and 3-16 are persuasive. The amendments entered on 4/18/2007 have been considered and the claims remain allowable. The feature of independent claims 1, 10, 11, 12, 15, and 16 directed towards allowable subject matter is the electroluminescent device having a driving transistor and switching transistor and setting and resetting step with a first and second sub horizontal period. The prior art of record, Yamazaki (366), teaches of said driving and switching transistor however said set and reset within the sub horizontal period as claimed is not taught. Yamazaki (366) also fails to specifically teach of said electroluminescent feature. Further, Yamazaki (656) fails to teach of said driving transistor as well as possessing the set and reset step with respect to said drive transistor as claimed. Yamazaki merely provides for a switching transistor. As argued by the Applicant Yamazaki (366) and Yamazaki (656) are not properly combined. Therefore said features of claims 1, 10, 11, 12, 15, and 16 in combination with the other limitations of the claims make said claims 1 and 3-16 allowable over the prior art of record.
- 4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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- 5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **David L. Lewis** whose telephone number is (571) 272-7673. The examiner can normally be reached on MTWTHF from 8 to 5. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala, can be reached on (571) 272-7681. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (571)-273-8300.
- 6. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Examiner: David L. Lewisc

June 7, 2007